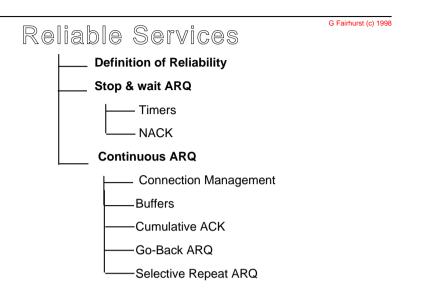
Reliability

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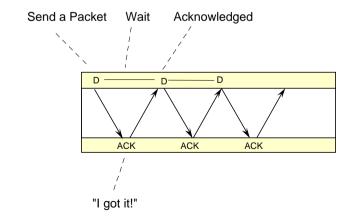


Implies....

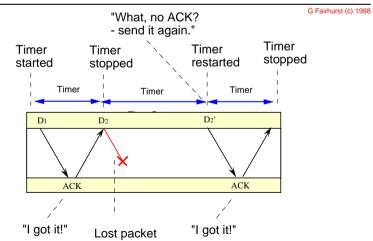
All information is received	(no loss, no residual errors)
No information is duplicated	(no extra copies)
Sequencing	(original order is preserved)

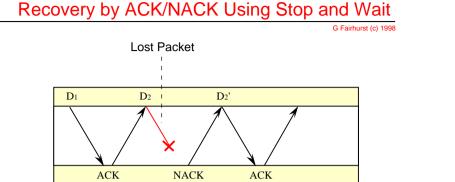
Stop and Wait / Idle ARQ

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Recovery by Timer Using Stop and Wait

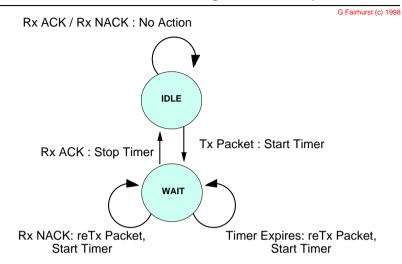




"Say that again"

(explicit NACK)

State Diagram for Stop and Wait



Stop & Wait G Fairhurst (c) 1998

"I got it!"

May be much faster than waiting for a timer

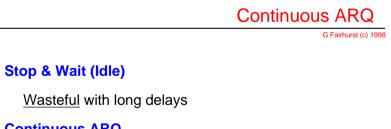
Advantages:

"I got it!"

Very simple to implement

Disadvantages:

- Response to every transmitted frame
- Half duplex operation
- Timers are needed to recover from loss of frames



Continuous ARQ

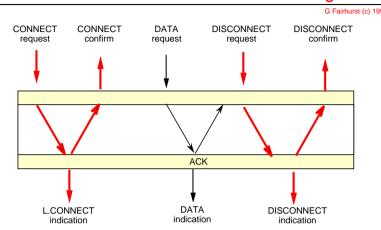
Uses a modulo sequence number

Numbers each packet to protect from duplication

Numbers ACKs/NACKs

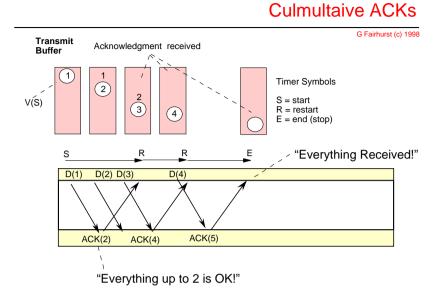
Requires buffers to store unacknowledged packets

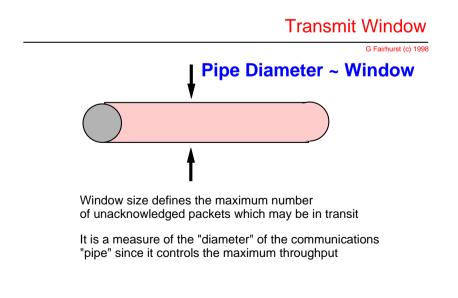
Requires connection management



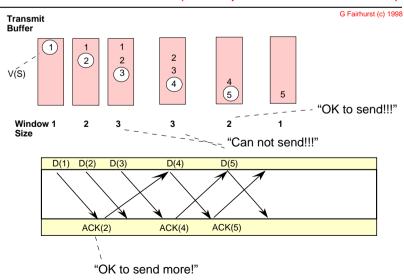
Connection Management

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Transmit Window (Example with Window = 3)

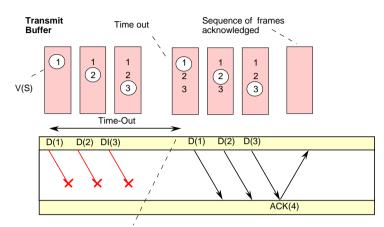


Transmit Timer

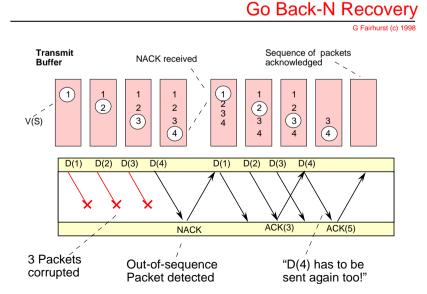
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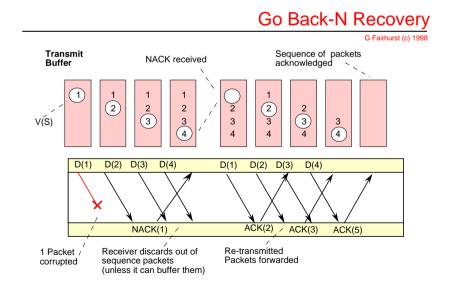
The transmit timer monitors the receipt of acknowledgements

- Starts: When a Data Packet sent, and not already running
- Restarts: When a new acknowledgment is received
- Stops: When all packets have been acknowledged



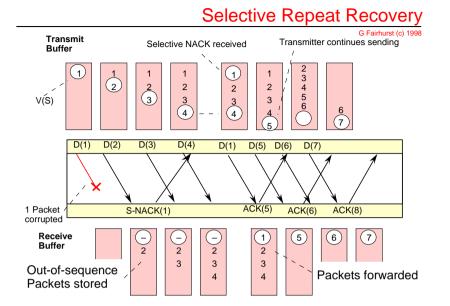
[&]quot;What no ACK yet!!"



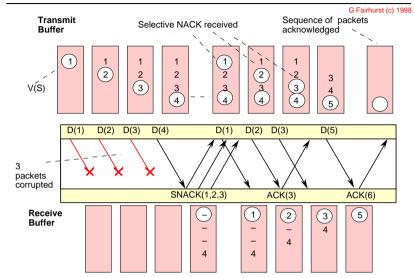


Time-Out Recovery

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Selective Repeat Recovery



Reliability

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Connection-Less	Connection-Oriented	
Best Effort	Reliable	
CRC Required	CRC Required	
Little setup required	Management Exchange	
No Confirmed Delivery	Acknowledgments	
No Retransmission	ARQ	

ARQ Comparison

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	S&W or Idle	Go-Back-N	Selective-Repeat
Protocol Design	Very simple	More Complex	Most Complex
Packet Types	I, ACK, NACK	I, ACK NACK (numbered)	I, ACK SNACK (numbered)
Buffer Requirements	One at Tx	Tx Window	Tx & Rx Window
Reliability?	Poss duplication	Reliable	Reliable
Timer?	Timer Needed	Timer Needed	Timer Needed
Efficiency	Low efficiency	Better efficiency with long delay x bandwidth	Best efficiency with long delay x bandwidth